

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10026085	FILING DATE 12/21/2001	CLASS 324	SUBCLASS 765	GAU <del>2850</del>	EXAMINER P. - Patel
<b>**APPLICANTS:</b> Watts Stephen; 3879					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>**FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				TI-29923	
Verified and Acknowledged Examiners's initials					
TITLE : Parallel integrated circuit test apparatus and test method					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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